

National Institute of Technology, Kurukshetra

Revised Notification for Result of Master of Technology III Semester School of VLSI Design and Embedded System (Embedded System Design) Examinations Nov./Dec.2024

The Result of the following candidates who appeared in Master of Technology III Semester School of VLSI Design and Embedded System (Embedded System Design) Examination of this Institute held in Nov./Dec.2024 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear


RL(A) For Result Late (Award)


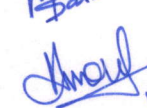
Sr. No.	Subjects	Code	No.
1	Preparatory Work for Dissertation	MSV2D33	

Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	323108109	Kundan Kumar	Shyambabu Sah	8.0000	7.8800
2	323108122	Karshni Kant Pathak	Gopi Kant Pathak	8.0000	7.9200

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 13 FEB 2025


Faculty in Charge
(Examinations)


Pravek

Anand
13/02/25