National Institute of Technology, Kurukshetra

Re-appear Notification for Result of Master of Technology III Semester School of VLSI Design and Embedded System (Embedded System Design) Examinations May/June 2025

The Result of the following candidates who appeared in Master of Technology III Semester School of VLSI Design and Embedded System (Embedded System Design) Examination of this Institute held in May/June 2025 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Sr. No. Subjects			Code	No.
1 Preparatory Work for Dissertation				MSV2D33	
Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	32219104	Aman Sandhu	Krishan Sandhu	9.0000	8.7600
2	32219111	Rohit Pandey	Suneel Pandey	9.0000	9.0200

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 2 6 JUN 2025