

National Institute of Technology, Kurukshetra

Re-appear Notification for Result of Master of Technology I Semester School of VLSI Design and Embedded System (VLSI Design) Examinations May/June 2025

The Result of the following candidates who appeared in Master of Technology I Semester School of VLSI Design and Embedded System (VLSI Design) Examination of this Institute held in May/June 2025 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects	Code	No.
1	Digital IC Design	MSV1C01	
2	Analog IC Design	MSV1C03	
3	Hardware Description Languages and FPGABased Design	MSV1C05	
4	VLSI Design Lab-I	MSV1L07	
5	Minor Project	MSV1P09	
6	Embedded System Fundamentals	MSV1E31	
7	Solid State Devices	MSV1E43	

Sr. No.	Roll No.	Name	Father's Name	SGPA
1	324408209	Prakanshu Pathak	Sunil Pathak	7.4167
2	324408214	Km. Renu Rai	Arvind Kumar Rai	7.0833

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date:

Faculty In-Charge

(Examinations)

Pratibha Rai 23/06/2025
Anand 23/06/25