

**NATIONAL INSTITUTE OF TECHNOLOGY
KURUKSHETRA-136119**

Scrutiny Result for Ph.D. admission in Odd Semester of the AY 2025-26

Name of the Department /School: SCHOOL OF VLSI DESIGN & EMBEDDED SYSTEMS

Sr. No.	Application No.	Name of the Candidate	Father's Name	Candidate Category	Admission Category	Part Time / Full Time	Eligible / Not Eligible	Exempted from the written test (Yes/No)	Remarks
1	113	Alka Parihar	Arvind Singh Parihar	General	General	Part-Time	Provisionally Eligible	Yes	1. One year experience certificate from current employer will be submitted on 10 Sept,2025. Allowed by Dean Acad on the basis of mail from the candidate.
2	223	Himanshu	Prem Datt	General	General	Full-Time	Provisionally Eligible	No	1. NET-2014 Qualified (not valid) 2. PG Marksheet not attached (submit at the time of written/interview)
3	22	Aniket Verma	Suresh Verma	OBC-NCL	OBC-NCL	Full-Time	Provisionally Eligible	Yes	1. Pursuing final year 4 th sem of PG degree. Final marksheet and degree certificate must be submitted on or before 30 th October 2025. 2. Latest (issued on or After 01 April 2025) OBC-NCL certificate required 3. GATE-2024 Qualified, NET- 2024 Qualified
4	94	Pallvi Gupta	Ramesh Chand Gupta	General	General	Full-Time	Eligible	No	1. GATE/NET not qualified

Prof. I/C (CCN) with a request to get it uploaded on the Institute Website

Copy to: **Dean (Academic)** for information


DRCC Chairman/HoD
17/06/2025