

DEPT. OF VLSI DESIGN AND SYSTEM ENGG.

TIME TABLE w.e.f 28.07.25

3 rd SEM 2025-26 (BTECH ME&VLSI)								
	1	2	3	4	5	6	7	8
	8.00	9.00	10.00	11.00	12.00	1.00	2.00	3.00
MON	MVPC-207 (Lab) (MV-1) CP		MVPC-201 GS	MVPC-202 TNS	MVPC-203 DKS		CSIC-221 (Lab) (MV-1) AMK	
TUE	MVPC-205 SM	CSIC-221 AMK	MVPC-207 (Lab) (MV-2) CP		MVPC-201 GS		MVPC-206 (Lab) (MV-1) DSY	
WED	MVPC-206 (Lab) (MV-2) DSY		MVPC-208 (Lab) (MV-1) GP		MVPC-201 GS		CSIC-221 (Lab) (MV-2) AMK	
THU	MVPC-205 SM	MVPC-204 GP	MVPC-202 TNS	CSIC-221 AMK	MVPC-203 DKS		MVPC-208 (Lab) (MV-2) GP	
FRI	MVPC-204 GP	MVPC-205 SM	MVPC-202 TNS	CSIC-221 AMK	MVPC-203 DKS		MVPC-204 GP	

Note: All Classes will be held on Room No. LHC-205

(Head of the Dept.)
Dept. of VLSI Design and System Engg.

(Time Table I/C)
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