

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology II Semester School of VLSI Design and Embedded System (VLSI Design) examinations May/June 2025 (Re-evaluation)

The Result of the following candidates who appeared in Master of Technology II Semester School of VLSI Design and Embedded System (VLSI Design) examination of this Institute held in May/June 2025 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects	Code	No.
1	Modeling of Semiconductor Devices	MSV1C02	
2	Low Power VLSI Design	MSV1C04	
3	VLSI Design Lab-II	MSV1L06	
4	Minor Project	MSV1P08	
5	VLSI Physical Design	MSV1E40	
6	VLSI Testing and Verification	MSV1E42	
7	Digital Signal Processing with FPGAs	MSV1E44	

Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	324408215	Annu Kumari	Sunder Singh	6.3889	6.9306

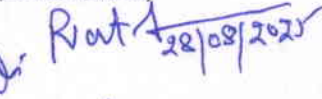
No change Result

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 28 AUG 2025


Faculty In-Charge

(Examinations)


28/08/2025

28/08/2025
