National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) examinations August, 2025

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) examination of this Institute held in August, 2025 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects		Code	No.
1	DISSERTATION			MSV2D44

Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	323108103	Diwakar Yadav	Ramkesh Yadav	9.0000	8.7656

Title of Dissertation : **Script Development to Automotate the Tx-compliance for 1G and 2.5G**

Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
2	323108104	Hemant Kumar	Satish Kumar	9.0000	8.9844

Title of Dissertation : **Design for Testability at the System-on-Chip level: Generation, Translation and Simulation of Chain, Stuck-at and At-speed Patterns**

Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
3	323108110	Utkarsh Srivastava	Sushil Srivastava	9.0000	8.2344

Title of Dissertation : **Memory Integration and Verification in Power Efficient CPU Architectures**

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date:

Faculty In-Charge

(Examinations)

14/08/2025