

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) Examinations August, 2025

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) Examination of this Institute held in August, 2025 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No. Subjects		Code			No.
1	DISSERTATION	MSV2D44			
Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	323108117	Saurabh Maurya	Late Mr.dinesh Kumar Maurya	9.0000	8.5781

Title of Dissertation : DFT Design Verification at SOC Leval

2	323108120	Bharti Poddar	Anil Poddar	9.0000	9.0469
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Title of Dissertation : Test Pattern Generation, Coverage & Run-time Optimization for Server SOC

3	323108122	Karshni Kant Pathak	Gopi Kant Pathak	8.0000	7.9375
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Title of Dissertation : Fast and Efficient Arithmetic Block Sealable Topology for AI/ML Applications

4	323408104	Satyam	Vinod Kumar Singh	9.0000	9.0000
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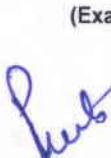
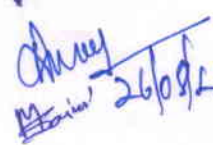
Title of Dissertation : Effective Clock Domain Crossing Violation Analysis and Code Quality Assurance in Million-Gate SoCs Using VC Lint and Verdi

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 26 AUG 2025


Faculty In-Charge

(Examinations)

 Rat 26/08/2025
 26/08/25