

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) examinations August, 2025

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) examination of this Institute held in August, 2025 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects	Code	No.
1	DISSERTATION		MSV1D10

Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	323108211	Abhishek Guglia	Mukesh Kumar	8.0000	7.6719

Title of Dissertation : Strain Tuned BP/MoSTe Heterostructure for Optoelectronic Application

2	323108231	Akash Gupta	Pradeep Kumar Gupta	9.0000	9.1563
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Title of Dissertation : Exploring the Art of VLSI Design : From APR Flow to Final Verification

3	323408201	Himanshu	Rajesh Kumar	9.0000	8.5156
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Title of Dissertation : Development of Analog and Digital Wafer Test Patterns for SOC Applications

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date:


Faculty in Charge
(Examinations)

Recd- Rat 14/08/2025
Anurag Kulkarni