

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) examinations August, 2025

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) examination of this Institute held in August, 2025 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects			Code	No.
1	DISSERTATION				MSV2D44
Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	32219111	Rohit Pandey	Suneel Pandey	8.0000	8.7969

Title of Dissertation : Analysis of Clock Trees for Optimization Through Multi Point Clock Tree Synthesis

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 09 SEP 2025

Faculty In-Charge
(Examinations)

Rohit
28/09/2025
Suneel