

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) examinations August, 2025

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) examination of this Institute held in August, 2025 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects	Code	No.
1	DISSERTATION		MSV2D44

Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	323108109	Kundan Kumar	Shyambabu Sah	9.0000	8.1250

Title of Dissertation : Digital Implementation of High-Speed PCIE Switch

2	323108111	Sunil Suman	Indradeo Ravidas	9.0000	8.8125
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Title of Dissertation : Trace & Trigger Integration for SOC Validation

3	323108112	Razique Ahmed	Khalique Ahmed	9.0000	9.0938
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Title of Dissertation : SMS Memory Built in Self-Test Verification

4	323108119	Dharmesh Ubhare	Bhaurao Ubhare	8.0000	7.8750
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Title of Dissertation : Comparative Study of Functional and Formal Verification Techniques

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 09 SEP 2025

Faculty In-Charge

(Examinations)

Prachi Raut 08/09/25
Anand 08/09/25