

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) examinations September, 2025

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) examination of this Institute held in September, 2025 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects	Code	No.
1	DISSERTATION	MSV2D44	

Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	323108114	Himanshu Rawat	Mohan Singh Rawat	8.0000	8.6875

Title of Dissertation : Physical Design on Advanced Technology Nodes: Implementation and Signoff of Multi-Partition High Performance SoC Using Fusion Compiler ICC2

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date:

23 SEP 2025

Faculty In Charge

(Examinations)

Preet Rathi 22/09/2025
Main
Shrey 22/09/25