

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) examinations August, 2025

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) examination of this Institute held in August, 2025 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

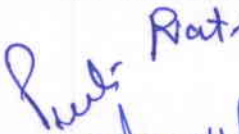
Sr. No.	Subjects		Code			No.
1	DISSERTATION					MSV2D44
Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA	
1	323108115	Srikant Suthar	Surendra Suthar	9.0000	8.6563	

Title of Dissertation : Analog Mixed Signal Simulations and Automation of the Flow Using Python Scripts

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 13 SEP 2025


Faculty in Charge
(Examinations)

 P. Rat 12/09/2025
 Anand 12/09/25
 Him