

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) Examinations August, 2025

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) examination of this Institute held in August, 2025 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects			Code	No.
1	DISSERTATION				MSV1D10
Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	323108202	Sanket Dharmeshbhai Atara	Dharmeshbhai Popatbhai Atara	9.0000	8.8438
Title of Dissertation :		Universal Verification Methodology Environment for SPI Master with Wishbone Interface			
2	323108226	Kotamsetti Jhansi	Kotamsetti Ramarao	8.0000	8.1875
Title of Dissertation :		Exploring the Interdependence of ESD Protection and Timing Optimization of Block Level Closure Using TSMC 5nm Tech Node in Fusion Compiler, Calibre			
3	323108229	Akansha Gautam	Ashok Kumar Gautam	8.0000	7.9375
Title of Dissertation :		Verification of IP Register in Test Chip Using AMBA APB			
4	323108230	Gulshan Kumar	Pappu Singh	9.0000	7.7500
Title of Dissertation :		Design of APB Interface for an IP Subsystem			
5	323108235	Prajwal Buddhadas Pillewan	Buddhadas	9.0000	7.7969
Title of Dissertation :		Advanced Techniques for Power Reduction and Timing Enhancement in VLSI Design			

13 SEP 2025

[Signature]

Rat 12/09/2025

6 323408205

Ujjawal Kulshreshtha

Ajay Narain Kulshreshtha

8.0000

7.3125

Title of Dissertation : **Energy Efficient Design of SPI Protocol for Low Power Embedded Systems**

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date:

13 SEP 2025

Faculty In-Charge

(Examinations)

Preeti Raut 12/09/2025
Ajay
Bair 12/09/25