

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) examinations August, 2025

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) examination of this Institute held in August, 2025 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No. Subjects		Code No.		
1	DISSERTATION	MSV1D10		
Sr. No.	Roll No.	Name	Father's Name	SGPA CGPA
1	323108206	Rahul Patel	Late Asharam Patel	8.0000 8.2188

Title of Dissertation : **Overview of Design for Testability, ATPG Flow, Pattern Generation and Pattern Translation**

2	323108209	Azharuddeen	Mohd Rais	8.0000	8.3281
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Title of Dissertation : **Predictive Timing Solutions for Route Stage in VLSI Physical Design with Machine Learning**

3	323408204	Aman Kumar	Brij Mohan	9.0000	8.1094
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Title of Dissertation : **Development and Validation of Physical Verification Rules Within the PDK for BCD Technology**

4	323408214	Karthik E	Erappa Gowda	9.0000	8.6875
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Title of Dissertation : **Enhancing Multivendor Frontend Simulation Flow with Regression Testing for Effective Design Verification**

**All efforts have been made to publish this result after checking the entries properly.
However, the result can stand revised in case some discrepancy is observed.**

Date: 23 SEP 2025

Faculty In Charge

(Examinations)

Preet Rat 22/09/2025
Main
Shal 25/9/25