

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) examinations August, 2025

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) examination of this Institute held in August, 2025 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects	Code	No.
1	DISSERTATION		MSV1D10

Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	323108219	Aayush Sharma	P R Sharma	8.0000	8.2344

Title of Dissertation : **Improvement in Quality of Result with Test Point Insertion i ATPG Process**

2	323108223	Saurabh Pandey	Sanjay Pandey	9.0000	8.5938
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Title of Dissertation : **Voltage-Aware Timing Signoff Through Dynamic IR Drop Integration Using RedHawk-SC**

3	323108224	Navneet Gupta	Santosh Kumar Gupta	9.0000	8.8750
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Title of Dissertation : **A Comprehensive Approach to Multivoltage ASIC Block Implementation Using 6nm Finfet Technology: Challenges and Optimizations**

4	323108232	Shivanshu Shrivastava	Niranjan Shrivastava	9.0000	8.3750
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Title of Dissertation : **Power Estimation of Block Using PTPX**

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09 SEP 2025

[Signature]

Rout 08/09/2025

5	323408212	Kalagoni Naga Sai Santosh	K Sreenivas Rao	9.0000	7.7188
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Title of Dissertation : **Design and FPGA Implementation of a PCM/FM Using SIMULINK
MATLAB and Vivado**

6	323408213	L G Naveen Kumar	L Govindaraju	9.0000	8.1406
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Title of Dissertation : **Low-Power Design Technique for Power Optimization in RISC-V
Processor**

**All efforts have been made to publish this result after checking the entries properly. However,
the result can stand revised in case some discrepancy is observed.**

Date:

09 SEP 2025

Faculty in Charge

(Examinations)

Prof. Ravi 08/09/2025
Hojin
J. M. 08/09/25