

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) examinations September, 2025

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) Examination of this Institute held in September, 2025 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects	Code	No.
1	DISSERTATION		MSV1D10

Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	323108233	Roma Gupta	Dilip Kumar Gupta	9.0000	7.8438

Title of Dissertation : Comprehensive Coverage Analysis and Verification of DFT Features with Industry Standard IEEE1687 Using ICL and PDL Methodology in SOC Design

2	323108234	Anamika Kaiwart	Yudhisthir Ram Kaiwart	8.0000	6.8906
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Title of Dissertation : UPF (Unified Power Format)-Based Power Optimization in Complex SoC Design

3	323408210	Mayank Pawar	Jagbir Singh	9.0000	7.6094
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Title of Dissertation : Design and Analysis of Low-Power 7T SRAM Cells Using Advanced Leakage Techniques

4	323408211	Pooja	Kartar Singh	8.0000	6.9844
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Title of Dissertation : Study of CNTFET Based Ternary Digital Circuits

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 13 SEP 2025

Faculty in Charge

(Examinations)

Pooja Rathi 12/09/2025
Anamika Kaiwart 12/09/25
Kartar Singh