

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) examinations September, 2025

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) examination of this Institute held in September, 2025 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects				Code	No.
1	DISSERTATION					MSV2D44
Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA	
1	323108106	Abhinav Ranjan Jha	Ajay Kant Jha	8.0000	8.5938	

Title of Dissertation : Enhancing DFX RTL Verification: Addressing Spyglass DFT Challenges

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date:

Faculty In-Charge
(Examinations)
Pratik Prati 23/10/2025
Shrey 23/10/25