

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) examinations September, 2025

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) examination of this Institute held in September, 2025 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects	Code	No.
1	DISSERTATION		MSV2D44

Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	323108107	Mayur Ranchhod Patil	Ranchhod Gokul Patil	9.0000	8.4375

Title of Dissertation : **Structured Verification of APB Protocol Signals and Register Access Using UVM and RAL Methodologies**

2	323108108	Swapan Kumar	Dindayal Kumar	9.0000	8.4219
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Title of Dissertation : **Execute-In-Place (XIP) with QSPI on Cortex-M7 MCUs Using NXP i.MX RT1160 Board**

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 18 OCT 2025


Faculty in-Charge

(Examinations)

Perk Raut 15/10/2025
Shradh
G. Viskols