

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) examinations October, 2025

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) examination of this Institute held in October, 2025 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects	Code	No.
1	DISSERTATION		MSV2D44

Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	323108113	Rahul Nagora	Jagdish	8.0000	7.7969

Title of Dissertation : **Impact of Interrupt Threshold Tuning on Microprocessor Power and Performance**

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date:

28 OCT 2025

Faculty In-Charge

(Examinations)

Pratik Raut 27/10/2025
Anand 27/10/25
M. Singh