

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) examinations September, 2025

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) examination of this Institute held in September, 2025 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

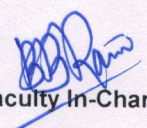
Sr. No.	Subjects	Code	No.
1	DISSERTATION		MSV2D44

Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	323108121	Dibyendu Das	Sukhomoy Das	8.0000	8.1250

Title of Dissertation : **Post Silicon Functionality Validation of Cache Coherence Block Using Python**

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date:


Faculty In-Charge

(Examinations)

Prat *Prat* *27/10/2025*
Sharma *27/10/25*
Sharma