

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) examinations October, 2025

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) examination of this Institute held in October, 2025 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects	Code	No.		
1	DISSERTATION		MSV1D10		
Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	323108212	Rahul Kumar Jaiswal	Govind Prasad Jaiswal	8.0000	7.4375

Title of Dissertation : **A First-Principles Study on Strain Engineering on Electronic and Optical Properties of HfSSe Bilayer**

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date:

18 OCT 2025

Faculty in Charge

(Examinations)

Pratibha Rastogi 15/10/2025
Anurag 15/10/25
Main