

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) examinations October, 2025

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) examination of this Institute held in October, 2025 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No. Subjects		Code		No.	
1	DISSERTATION			MSV1D10	
Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	323408207	Sandeep Thakur	Sher Singh	10.0000	8.1875

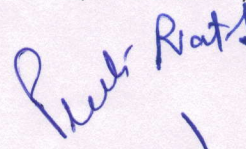
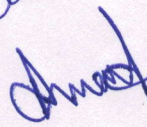
Title of Dissertation : **Modular Design of Area and Cost Efficient Leading Zero Counter in Quantum-dot Cellular Automata Nanotechnology**

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 28 OCT 2025


Faculty In-Charge

(Examinations)


Preet Rathi

Anand

Bhanu
27/10/2025