

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) examinations September, 2025

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) examination of this Institute held in September, 2025 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects			Code	No.
1	DISSERTATION				MSV1D10
Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	323108215	Madhu	Moti Ram	6.0000	8.5781

Title of Dissertation : Implementation of Spiking Neural Network with Lifelong Learning Ability

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date:

04 OCT 2025

Faculty in Charge

(Examinations)

Pratik Raut 03/10/2025
Anand 03/10/2025