

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) Examinations October, 2025

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) Examination of this Institute held in October, 2025 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No. Subjects		Code		No.	
1	DISSERTATION			MSV2D44	
Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	32219104	Aman Sandhu	Krishan Sandhu	8.0000	8.5938

Title of Dissertation : A High Gain Three-Stage CMOS Op Amp for Low Power Biomedical Sensors Using 90nm Technology

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 15 NOV 2025

Faculty in Charge

(Examinations)

Prateek Rastogi 14/11/2025