

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) Examinations October, 2025

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) Examination of this Institute held in October, 2025 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects	Code	No.
1	DISSERTATION	MSV2D44	

Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	323108101	Aritra Patra	Balaram Patra	9.0000	9.2031

Title of Dissertation : Verification of APB Protocol in Performance Monitoring Subsystem

2	323108102	Shashank Sonal	Kanchan Kumar	8.0000	9.2344
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Title of Dissertation : Implementation and UVM Based Verification of SerDes (Serializer/Deserializer) Interface for Efficient & High-Speed Data Transmission

3	323108116	Anurag Verma	Atul Kumar Verma	9.0000	9.0469
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Title of Dissertation : Physical Design Implementation and Physical Verification of a High Performance CPU at Advanced Technology node

4	323108118	Muzaffar Ali Khan	Munawwar Ali Khan	9.0000	8.7344
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Title of Dissertation : Power Stress Validation of Motherboard Voltage Regulations on Latest Intel Xeon Server SoC Platforms

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 06 NOV 2025

Faculty in Charge
(Examinations)

06/11/2025