

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) Examinations October, 2025

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) Examination of this Institute held in October, 2025 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects			Code	No.	
1	DISSERTATION				MSV1D10	
Sr. No.	Roll No.	Name		Father's Name	SGPA	CGPA
1	323108205	Deepansh Gupta		Dilip Gupta	8.0000	8.5625

Title of Dissertation : Hierarchical Test Compression & Pattern Migration Flow

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 15 NOV 2025

Faculty In-Charge
(Examinations)

Pratishtha
14/11/2025