

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) examinations August, 2025

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) Examination of this Institute held in August, 2025 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No. Subjects		Code		No.	
1	DISSERTATION			MSV1D10	
Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	323108207	Veera Krishna Vamsi Rednam	Rednam Ananda Rao	9.0000	8.8438
Title of Dissertation :		Clock Domain Crossing Issue in Multi Clock And Multi Reset SoC			
2	323108228	Dyanand Dev	Narendra Dev	10.0000	8.7969
Title of Dissertation :		Insertion And Verification of Memory Built-In Self-Test & BIRA Architectures in High-Performance Multi-Cor CPUs			
3	323408206	Sneha Bhattacharya	Manas Bhattacharya	10.0000	8.4063
Title of Dissertation :		Efficient PDN Sign-off With EMIR Analysis: Redhawk-SC Workflows For Next-Gen Semiconductors			

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 15 NOV 2025

Faculty in-Charge
(Examinations)

Pratik Rat 14/11/2025