

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) examinations October, 2025

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) examination of this Institute held in October, 2025 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No. Subjects		Code No.		
1	DISSERTATION		MSV1D10	
Sr. No.	Roll No.	Name	Father's Name	SGPA CGPA
1	323408202	Dushyant	Jagdish Kumar	9.0000 8.2188

Title of Dissertation : SoC Integration of Digital IPs and Running RTL Based Signoff Tools in an ARM Microcontroller

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 06 NOV 2025


Faculty In-Charge

(Examinations)

 Rishi 06/11/2025
 Anshu 06/11/25