

TIME TABLE — M.Tech VLSI Design (1st Sem)								
M.Tech VLSI Design (1st Sem) (60 students). Lunch: 12:00-1:00 PM								
Time	8.3	9.25	10.4	11.35	12.3	1.45	2.4	3.35
Lecture	1	2	3	4	5	6	7	8
Mon	MVDC103/MVDE221 LHC-306 Dr. Gaurav Saini	MVDL101L (B2) VLSI_Lab Dr. Ashutosh Nandi				LUNCH	MVDP101 Dr. Ankush Vashistha/Dr. D. S. Yadav	MVDP101 Dr. Ankush Vashistha/Dr. D. S. Yadav
Tue	MVDE113/MVDC203 LHC-306 Dr. D. S. Yadav	MVDC105/205 LHC-306 Prof. R. K. Sharma	MVDE109/MVDE207 LHC-306 Dr. Dablu Kumar		LUNCH	MVDC101/MVDE211 LHC-304 Dr. Ashutosh Nandi		MVDE109/MVDE207 LHC-306 Dr. Dablu Kumar
Wed	MVDE113/MVDC203 LHC-304 Dr. D. S. Yadav	MVDC105/205 LHC-306 Prof. R. K. Sharma	MVDC105/205 LHC-306 Prof. R. K. Sharma	MVDC103/MVDE221 LHC-306 Dr. Gaurav Saini	LUNCH	MVDC103/MVDE221 LHC-304 Dr. Gaurav Saini	MVDE101/MVDC201 LHC-304 Dr. Ankush Vashistha	MVDE101/MVDC201 LHC-306 Dr. Ankush Vashistha
Thu	MVDL101L (B1) VLSI_Lab Dr. Gaurav Saini				LUNCH	MVDC101/MVDE211 LHC-306 Dr. Ashutosh Nandi	MVDE109/MVDE207 LHC-304 Dr. Dablu Kumar	MVDE113/MVDC203 LHC-306 Dr. D. S. Yadav
Fri	MVDE101/MVDC201 LHC-306 Dr. Ankush Vashistha	MVDS101 Dr. Dablu Kumar			LUNCH	MVDC105/205 LHC-304 Prof. R. K. Sharma		