

National Institute of Technology, Kurukshetra

Re-appear Notification for Result of Master of Technology II Semester VLSI Design & Systems Engineering (VLSI Design) examinations JULY/AUGUST-2026 (Special Reappear Exam)

The Result of the following candidates who appeared in Master of Technology II Semester VLSI Design & Systems Engineering (VLSI Design) examination of this Institute held in JULY/AUGUST-2026 (Special Reappear Exam) is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects	Code	No.
1	Modeling of Semiconductor Devices	MVDC102	
2	Low Power VLSI Design	MVDC104	
3	VLSI Testing and Verification	MVDC106	
4	Mixed Signal IC Design	MVDE104	
5	VLSI Physical Design	MVDE110	
6	Semiconductors Memories	MVDE114	
7	VLSI Design Lab-II	MVDL102	
8	Minor Project-II	MVDP102	
9	Seminar	MVDS102	

Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	325108227	Surjan Singh Jony	Dalip Singh	Re. MVDC104 (End)	RL (L)
2	325108232	Vishal Kumar Gautam	Paras Nath Gautam	Re. MVDC104 (End)	RL (L)
3	325408220	Akriti	Chandragupt Kumar Nirala	Re. MVDC104 (End)	RL (L)

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 15-September-2026

Faculty in Charge

(Examinations)

Revised 15/09/2026