

## National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) examinations August, 2026

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) examination of this Institute held in August, 2026 is declared as under:-

**Note: SGPA shown means Pass and "R" means Reappear**

### RL(A) For Result Late (Award)

| Sr. No. | Subjects     | Code    | No. |
|---------|--------------|---------|-----|
| 1       | DISSERTATION | MSV2D44 |     |

| Sr. No. | Roll No.  | Name         | Father's Name     | SGPA     | CGPA     |
|---------|-----------|--------------|-------------------|----------|----------|
| 1       | 324108120 | Harish Rawat | Anoop Singh Rawat | 9.0000 ✓ | 8.2813 ✓ |

Title of Dissertation : **Low-Power DFT Architecture and Verification Algorithm for Power Aware Designs** ✓

**All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.**

Date: 28-September-2026

Faculty in Charge

(Examinations)

28/09/2026

P. K. Taniya 28/09/26