

National Institute of Technology, Kurukshetra

Re-appear Notification for Result of Master of Technology II Semester VLSI Design and Systems Engineering (Embedded System Design) examinations JULY/AUGUST-2026 (Special Reappear Exam)

The Result of the following candidates who appeared in Master of Technology II Semester VLSI Design and Systems Engineering (Embedded System Design) examination of this Institute held in JULY/AUGUST-2026 (Special Reappear Exam) is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects	Code	No.
1	Real Time Systems	MVDC202	
2	Hardware Software Co-design	MVDC204	
3	Embedded Microcontrollers and Applications	MVDC206	
4	Wireless Sensor Networks	MVDE204	
5	Mixed Signal IC Design	MVDE208	
6	Low Power VLSI Design	MVDE212	
7	Embedded Application Lab	MVDL202	
8	Minor Project-II	MVDP202	
9	Seminar	MVDS202	

Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	325108109	Badavath Prem Kumar	Badavath Badya	Re. MVDE212 (End)	RL (L)
2	325108119	Ka. Patel Neel Alpeshkumar	Alpeshkumar	7.8542	7.9271
3	325408107	Vijaya Dwivedi	Sanjay Kumar Dwivedi	9.5000	9.0313

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 14-September-2026

Faculty In Charge

(Examinations)

D. K. Rastogi 14/09/2026