National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) examinations October, 2026

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) examination of this Institute held in October, 2026 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects	Code	No.
1	DISSERTATION		MSV1D10

Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	324108232	Sachin Kumar	Bijendra Pal Singh	9.0000	8.3203

Title of Dissertation : **Design and Analysis of Low-Power LFSR Architectures for BIST Applications: Power- Hardware Trade-off**

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 27-October-2026

Faculty In-Charge

(Examinations)

28/10/2026

Taniya 25/10/26