National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) Examinations September, 2026

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) Examination of this Institute held in September, 2026 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects	Code No.
1	DISSERTATION	MSV1D10

Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	324108203	Venneti Venkata Sai Bharadhwaj	V Ram Kumar	8.0000	8.5078

Title of Dissertation : **Efficient RTL to GDSII Layout Implementation and Verification of a 5-stage Pipelined RISC-V**

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 28-October-2026

Faculty In-Charge

(Examinations)

30/10/2026

30/10/26